In the Specification:

Kindly replace the paragraph entitled "CROSS-REFERENCE TO RELATED APPLICATIONS" with the following revised paragraph:

the present application is related to the following co-pending and commonly assigned
United States patent applications, which are hereby incorporated by reference in their respective
entirety: serial number 09/675,779, filed September 29, 2000, now Patent No. 6,834,337 B1
filed on by Mitchell et al. for "System and Method for Enabling
Multiple Signed Independent Data Elements per Register" (IBM docket number
BLD920000060); serial number 09/570,382 filed on May 12, 2000, now Patent No. 6,970,179
<u>B1</u> by T. J. Trenary et al. for "Method and Apparatus for the Scaling Up of Data"; serial number
09/570,849 filed on May 12, 2000, now Patent No. 7,062,098 B1 by J. L. Mitchell et al. for
"Method and Apparatus for the Scaling Down of Data"; serial number 09/694,448, filed October
23, 2000, now Patent No. 6,766,341 B1 filed on by Trelewicz et al.
for "Faster Transforms Using Scaled Terms" (IBM docket number BLD920000059); serial
number 09/535,587, filed March 27, 2000, now Patent No. 7,064,859 B1 by D. K. Dittrich et al
for "Method, System, Program, and Data Structure for Producing a Look-Up Table to Enhance
Print Quality" filed on by Trenary et al. for "Reduction of
N DCT blocks into One Block" (IBM docket number BLD919990036); and serial number
10/198,097, filed July 19, 2002, now Patent No. 7,099,523 B2 filed on
by Tomasz Nowicki et al for "Method and System for Scaling a Signal Sample
Rate" (IBM docket number YOR920020113US1).

Kindly replace the paragraph beginning at line 6 of page 15 with the following revised paragraph:

Referring now to Figure 5, an embodiment of the invention described above may be tangibly embodied in a in a computer program residing on a computer-readable medium or carrier 490. The medium 490 may comprise one or more of a fixed and/or removable data storage device such as a floppy disk or a CD-ROM, or it may consist of some other type of data storage or data communications device. The computer program may be loaded into the memory 492 to configure the processor 440 for execution. The computer program comprises instructions which, when read and executed by the processor 440 causes the processor 440 to perform the steps necessary to execute the steps or elements of the present invention.